

CLIPPEDIMAGE= JP407202477A  
PAT-NO: JP407202477A  
DOCUMENT-IDENTIFIER: JP 07202477 A  
TITLE: PRINTED BOARD FOR IMPROVING ELECTROMAGNETIC WAVE  
INTERFERENCE

PUBN-DATE: August 4, 1995

INVENTOR-INFORMATION:

NAME

FUJIMOTO, SHUNSUKE

ASSIGNEE-INFORMATION:

NAME

NEC CORP

COUNTRY

N/A

APPL-NO: JP05334735

APPL-DATE: December 28, 1993

INT-CL\_(IPC): H05K009/00; H05K001/02

ABSTRACT:

PURPOSE: To take a perfect countermeasure against electromagnetic wave interference by preventing radiation of noise in the direction of thickness of a printed board thereby enhancing the shielding effect of the printed board unit.

CONSTITUTION: A through hole 5 connected with a ground layer is made along the fringe of a printed board and a pattern connected therewith or a pad 7 for a capacitor connected with the through hole 5 is provided. This structure can shield noise radiated from the direction of thickness of the printed board. Consequently, radiation of noise from the printed board can be reduced and the countermeasure against electromagnetic interference wave can be taken more perfectly.

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